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EFS ID:	1669560
Application Number:	09754406
International Application Number:	
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Title of Invention:	METHODOLOGY AND APPLICATIONS OF TIMING-DRIVEN LOGIC RESYSNTHESIS FOR VLSI CIRCUITS
First Named Inventor/Applicant Name:	Songjie Xu
Customer Number:	20995
Filer:	Raimond J Salenieks/Glen Martell
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2	Fee Worksheet (PTO-06)	fee-info.pdf	8200	no	2
Information:			-		
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	Miscellaneous Incoming Letter		3	3	
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